

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: U.S. Patent No. 7,607,053

USPTO CONFIRMATION CODE: 1367

APPLICATION NO.: 10/596,856

PCT FILED: December 30, 2003

U.S. FILED: June 27, 2006

EXAMINER: Shelly A. Chase

GROUP ART UNIT: 2112

FOR: METHOD AND DEVICE FOR CALCULATING BIT ERROR RATE OF
RECEIVED SIGNAL

37 CFR 1.322 & 37 CFR 1.323 REQUEST FOR CERTIFICATE OF CORRECTION
FOR USPTO AND/OR APPLICANT MISTAKE

HONORABLE COMMISSIONER OF PATENTS & TRADEMARKS

SIR:

The following is a request for a certificate of correction in Serial Number 10/596,856, now Patent Number 7,607,053.

A certificate of correction under 35 USC 254 is respectfully requested in the above-identified patent.

The errors were the fault of both the applicant and USPTO and, accordingly, please charge \$100.00 to our Deposit Account No. 50-1379. In the event that a further fee is required, please charge the amount to the same Deposit Account.

The exact locations where the errors appear in the patent and patent application are as follows:

In Column 5, Line 62, after “BER” insert - - S10. - -.
(ORIGINALLY FILED SPECIFICATION DATED JUNE 27, 2006, PAGE 10
(PAGE 59 OF FW), LINE 13)

In Column 6, Line 12, delete “105” and insert - - 105 is - -, therefor.
(ORIGINALLY FILED SPECIFICATION DATED JUNE 27, 2006, PAGE 10
(PAGE 59 OF FW), LINE 23)

In Column 7, Line 34, in Claim 1, delete “Bit error Rate” and insert - - Bit Error
Rate - -, therefor.
(AMENDMENTS TO THE CLAIMS DATED JUNE 27, 2006, PAGE 2 OF 6,
CLAIM 1, LINE 15)

In Column 8, Line 31, in Claim 13, delete “decoder” and insert - - decoder, - -,
therefor.
(AMENDMENTS TO THE CLAIMS DATED JUNE 27, 2006, PAGE 4 OF 6,
CLAIM 13, LINE 3)

The requested corrections are attached on Form PTO 1050.

Respectfully Submitted

, 2010

DATE

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